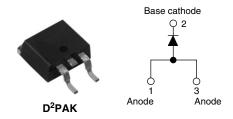




Vishay High Power Products

Input Rectifier Diode, 10 A



PRODUCT SUMMARY		
V _F at 10 A	< 1 V	
I _{FSM}	200 A	
V _{RRM}	800/1200 V	

DESCRIPTION/FEATURES

The 10ETS..S rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The glass passivation technology used has reliable operation up to 150 °C junction temperature.

Typical applications are in input rectification and these products are designed to be used with Vishay HPP switches and output rectifiers which are available in identical package outlines.

This product series has been designed and qualified for industrial level.

OUTPUT CURRENT IN TYPICAL APPLICATIONS				
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS	
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C common heatsink of 1 °C/W	12.0	16.0	А	

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Sinusoidal waveform	10	Α	
V _{RRM}		800/1200	V	
I _{FSM}		200	А	
V _F	10 A, T _J = 25 °C	1.1	V	
T _J		- 40 to 150	°C	

VOLTAGE RATINGS					
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA		
10ETS08S	800	900			
10ETS10S	1000	1100	0.5		
10ETS12S	1200	1300			

ABSOLUTE MAXIMUM RATIN	IGS			
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current	I _{F(AV)}	$T_C = 105$ °C, 180 ° conduction half sine wave	10	
Maximum peak one cycle		10 ms sine pulse, rated $V_{\mbox{\scriptsize RRM}}$ applied	170	Α
non-repetitive surge current	I _{FSM}	10 ms sine pulse, no voltage reapplied	200	
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	130	A ² s
iviaximum i-t for fusing	1-1	10 ms sine pulse, no voltage reapplied	145	A-S
Maximum I ² √t for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied	1450	A²√s

Document Number: 93486 Revision: 26-Aug-08

10ETS..S High Voltage Series

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST (CONDITIONS	VALUES	UNITS
Maximum forward voltage drop	V_{FM}	10 A, T _J = 25 °C		1.1	V
Forward slope resistance	rt	T _{.1} = 150 °C		20	mΩ
Threshold voltage	V _{F(TO)}	1) = 150 C		0.82	V
Maximum rayaraa laakaga aurrant		T _J = 25 °C	V - Potod V	0.05	mA
Maximum reverse leakage current I _{RM}	T _J = 150 °C	V _R = Rated V _{RRM}	0.50	IIIA	

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 40 to 150	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	2.5	°C/W
Maximum thermal resistance, junction to ambient (PCB mount)	R _{thJA} (1)		62	C/VV
Soldering temperature	Ts		240	°C
Approximate weight			2	g
Approximate weight			0.07	OZ.
			10ET	S08S
Marking device		Case style D ² PAK (SMD-220)	10ET	S10S
			10ET	S12S

Note

 $^{^{(1)}}$ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μm) copper 40 °C/W For recommended footprint and soldering techniques refer to application note #AN-994



Input Rectifier Diode, 10 A Vishay High Power Products

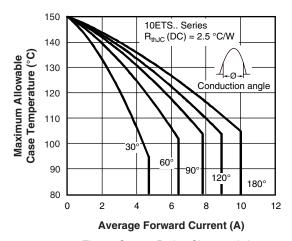


Fig. 1 - Current Rating Characteristics

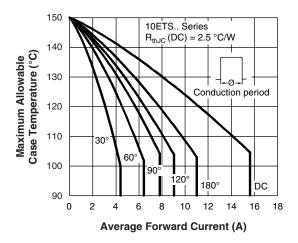


Fig. 2 - Current Rating Characteristics

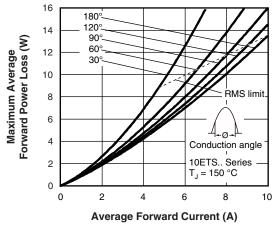


Fig. 3 - Forward Power Loss Characteristics

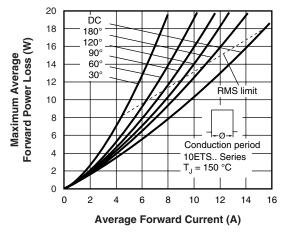


Fig. 4 - Forward Power Loss Characteristics

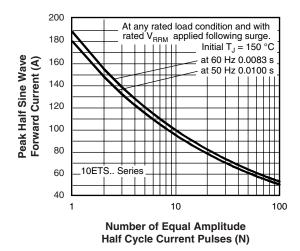


Fig. 5 - Maximum Non-Repetitive Surge Current

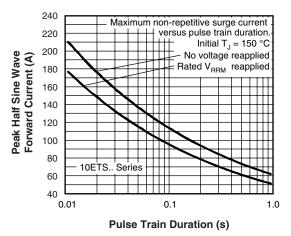


Fig. 6 - Maximum Non-Repetitive Surge Current

Vishay High Power Products Input Rectifier Diode, 10 A



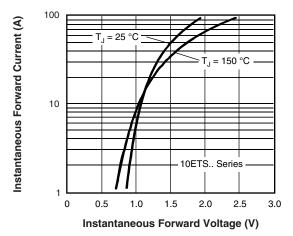


Fig. 7 - Forward Voltage Drop Characteristics

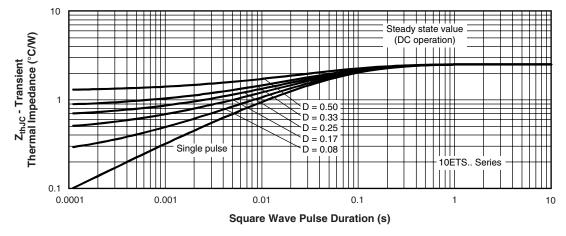


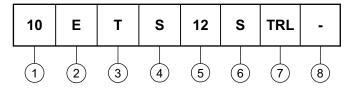
Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



Input Rectifier Diode, 10 A Vishay High Power Products

ORDERING INFORMATION TABLE

Device code



Current rating (10 = 10 A)

Circuit configuration

E = Single diode

3 Package

T = TO-220AC

4 Type of silicon

S = Standard recovery rectifier

08 = 800 V 10 = 1000 V

Voltage code x $100 = V_{RRM}$

 $S = TO-220 D^2PAK (SMD-220) version$

12 = 1200 V

• None = Tube

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

8 • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95046			
Part marking information	http://www.vishay.com/doc?95054		
Packaging information	http://www.vishay.com/doc?95032		

Document Number: 93486 Revision: 26-Aug-08



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Revision: 18-Jul-08

Document Number: 91000 www.vishay.com